

REMARKS

This amendment is submitted in response to an Office Action mailed April 4, 2007. Applicant respectfully requests reconsideration of the subject application as amended herein.

Claims 1-3 and 5-7 remain in the present application. Claims 4 and 8-20 have been canceled without prejudice. Claim 21 has been added to the present application.

In the April 4, 2007 Office Action, claims 1-3, 5-11, 19, and 20 were rejected under 35 U.S.C. § 102(b) as being anticipated by U.S. Patent No. 6,178,022 issued to Yoneyama (hereinafter "Yoneyama"). Claims 8-11, 19, and 20 have been canceled from the present application, thereby rendering the rejection of such claims moot. Applicant has amended the remaining claims 1-3 and 5-7 to distinguish over Yoneyama. For example, amended claim 1 states:

An apparatus comprising:

- a processor;
- a plurality of laser drivers in the processor, each of said plurality of laser drivers to generate an electrical data signal at a corresponding one of a plurality of output ports distributed on a surface of the processor;
- a laser unit coupled to the surface of the processor, said laser unit having a plurality of input ports, each of the plurality of input ports bonded to one of the output ports distributed on the surface of the processor package, said laser unit including at least one pulse laser to generate at least one pulse train; and
- said laser unit including a plurality of laser modulators, each of said plurality of laser modulators to receive a particular pulse train from the at least one pulse laser, and a particular electrical data signal from a corresponding one of the plurality of laser drivers, each of said plurality of laser modulators to encode its received electrical data signal onto its received pulse train by selectively passing pulses.

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Amended claim 1 is limited to a particular circuit arrangement for optical communications. The novel circuit arrangement includes an array of laser drivers on a surface of a processor to drive an array of laser modulators that modulate at least one laser pulse train.

Applicant respectfully submits that Yoneyama does not suggest or disclose the particular circuit arrangement of amended claim 1. Therefore, Applicant respectfully submits that amended claim 1 is not anticipated by Yoneyama.

Applicant submits that the reasoning presented above with respect to amended claim 1 similarly applies to claims 2-3 and 5-7, as amended. Thus, for at least the reasons discussed above, Applicant respectfully submits that claims 2-3 and 5-7 are likewise not anticipated by Yoneyama.

In the April 4, 2007 Office Action, claim 4 was rejected under 35 U.S.C. § 103 as being unpatentable over Yoneyama. Claim 4 has been canceled from the present application, thereby rendering the rejection of claim 4 moot.

In the April 4, 2007 Office Action, claims 12-14 were rejected under 35 U.S.C. § 103 as being unpatentable over Yoneyama in view of U.S. Patent Application 2003/0002118 by Givehchi, further in view of U.S. Patent Application 2001/0050793 by Harpin et al. Claims 12-14 have been canceled from the present application, thereby rendering the rejection of such claims moot.

New claim 21 depends from amended claim 21. Therefore, for at least the reasons discussed above, Applicant respectfully submits that new claim 21 is not anticipated by Yoneyama.

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In conclusion, Applicant respectfully submits that claims 1-3, 5-7, and 21 are now in a condition for allowance, and Applicant respectfully requests allowance of such claims.

Please charge any shortages and credit any overages to our Deposit Account No. 50-0221.

Respectfully submitted,

INTEL CORPORATION

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